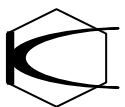


PCB LAYOUT

NOTE : TOLERANCE FOR PCB LAYOUT IS ± 0.05

- NOTES:
- 1.0. ELECTRICAL CHARACTERISTIC:
 - 1.1. LOW LEVEL CONTACT RESISTANCE:INITIAL:
30mΩ MAXIMUM FOR POWER AND GROUND CONTACTS.
INITIAL:50mΩ MAXIMUM
FOR ALL OTHER CONTACTS.
AFTER:10mΩ MAX I MUM CHANGE.
 - 1.2. DIELECTIC WITHSTANDING VOLTAGE:
100V AC FOR ONE MINUTE AT SEA LEVEL.
 - 1.3. INSULATION RESISTANCE:100MΩ MINIMUM.
 - 2.0. MECHANICAL CHARACTERISTIC:
 - 2.1. INSERTION FORCE:35N(3.57kg)MAXIMUN.
 - 2.2. EXTRACTION FORCE:INITIAL:10N(1.02kg)MINIMUM.
AFTER:8N(0.816kg)MINIMUM.

3	SHELL	SPCC:PLATED 銅底 100U"MIN,鍍銀 100U"MIN	1
2	HOUSING	LCP E130i ; COLOR:BLUE	1
1	CONTACT	C2680R-EH;PLATED:鍍銀底 50U"MIN,接觸點鍍金 30U"MIN;鍍亮錫 100U"MIN	1 SET(9PCS)
NO	PART NAME	MATERIAL	Q' TY



銓基電子股份有限公司
ROKI ELECTRONICS CO., LTD.

PART NAME : USB3.0 JACK

PART NO : USB3.0-AM17.65

TOLERANCE : X±0.25 mm X.X±0.15mm X.X.X±0.1mm

SCALE :

UNIT : mm

VER : R01

